Branch Prediction

Philipp Koehn

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Control Hazard



- Also called branch hazard
- Selection of next instruction depends on outcome of previous
- Example

beq \$s0, \$s1, ff40
sub \$t0, \$s0, \$t3

- sub instruction only executed if branch condition fails
- \rightarrow cannot start until branch condition result known

Methods



- Assume branch not taken
 - start execution of following instructions
 - if wrong, flush them
- Reduce delay of branches
 - compute branch address and condition in fewer cycles
 - \rightarrow less flushing
- Dynamic branch prediction



assume branch not taken

Pipelined Datapath





Branch: Address Calculation





Branch: Condition Checking









- Assume branch not taken
- Execute the subsequent instructions
- If branch should have been taken
 - \rightarrow flush out subsequent instruction processing























Flush Instructions





Flush Instructions



- Change program counter to target addr
- Instructions in stages IF, ID, EX, MEM
- \Rightarrow Re-fetch instruction in IF branch farget
- \Rightarrow Zero out control lines for ID, EX, MEM



fast branch execution

Idea



• Branch instruction

```
beq $s0, $s1, ff40
bne
```

- Computations required
 - target address (PC + specified offset)
 - condition check (simple equality)
- Idea: carry out these computations quickly

```
- in ID
```

Branch Computation in ID Stage



- Role of ID stage
 - decode what the instruction is
 - look up register values
- Now
 - just assume that it is a branch
 - add special wiring for branch calculation
 - if not a branch, ignore results
- Benefit: reduce branch flushing from 3 to 1 instruction

Pipeline with Hazard Detection Unit



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Register Values for Condition Check





Address Calculation









- Condition check requires register values
- These may be changed by preceding instructions in pipeline
- \Rightarrow Forwarding and stalling needed
 - Adds complexity



branch prediction

Prediction



- So far: predict branch not taken
- \bullet Compiler may order instructions \rightarrow more frequent case in sequence
- \bullet Now: dynamic branch prediction based on branch history table \mathcal{W} execution history

Branch History Table



- Idea: keep record of branch history
- Many branches, many executions

Branch History Table



- Idea: keep record of branch history
- Many branches, many executions
- Keep it simple:
 - index by lower order bits of branch address (ignore collisions)
 - just store last decison (1 bit)
- Special memory in ID stage

Example: Loop



- Example: Loop 9 times, then exit loop
- False predictions
 - last iteration (not taken, after 9 times taken)
 - first iteration (taken, previously exited loop)
- Prediction accuracy: 8/10 = 80%
- Can we do better?

2 Bits



• Idea: record frequency



• Previous example (loop 9 times, then exit loop)

Iteration	Value	Prediction
1	2	take branch (correct)
2	3	take branch (correct)
9	3	take branch (correct)
10	3	take branch (wrong)