Lecture 2: Data representation, addresses

Phillipp Koehn, David Hovemeyer

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601.229 Computer Systems Fundamentals

- \blacktriangleright Today:
	- \blacktriangleright Data representation
	- \blacktriangleright Addresses
	- \blacktriangleright Bitwise operations

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Data representation

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There are only $\mathbf{10}$ kinds of people. Those who understand binary and those who don't.

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Let's consider ways of representing numbers...

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I V X L C D M 1 5 10 50 100 500 1000

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I V X L C D M 1 5 10 50 100 500 1000

 \blacktriangleright Additive combination of units II III VI XVI XXXIII MDCLXVI MMXVI

KO K K G K K E K E K K H K K K K K K K K K

I V X L C D M 1 5 10 50 100 500 1000

 \blacktriangleright Additive combination of units II III VI XVI XXXIII MDCLXVI MMXVI 2 3 6 16 33 1666 2016

I V X L C D M 1 5 10 50 100 500 1000

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 \blacktriangleright Additive combination of units II III VI XVI XXXIII MDCLXVI MMXVI 2 3 6 16 33 1666 2016

 \triangleright Subtractive combination of units IV IX XL XC CD CM MCMLXXI

I V X L C D M 1 5 10 50 100 500 1000

 \blacktriangleright Additive combination of units II III VI XVI XXXIII MDCLXVI MMXVI 2 3 6 16 33 1666 2016

 \triangleright Subtractive combination of units

IV IX XL XC CD CM MCMLXXI 4 9 40 90 400 900 1971

- ▶ Developed in India and Arabic world during the European Dark Age
- ▶ Decisive step: invention of zero by Brahmagupta in AD 628
- \blacktriangleright Basic units

0 1 2 3 4 5 6 7 8 9

 \blacktriangleright Positional system

1 10 100 1000 10000 100000 1000000

Why Base 10?

$dig·it$ /'dijit/ (i)

noun

- 1. any of the numerals from 0 to 9, especially when forming part of a number. synonyms: numeral, number, figure, integer "the door code has ten digits"
- 2. a finger (including the thumb) or toe. synonyms: finger, thumb, toe; extremity "we wanted to warm our frozen digits"

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Computer hardware is based on digital logic

 \triangleright where *digital voltages* (high and low) represent 1 and 0

Binary number 1 1 0 1 0 1 0 1

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\triangleright Numbers like 11010101 are very hard to read

⇒ Octal numbers

Binary number 1 1 0 1 0 1 0 1 —– ——– ——– Octal number 3 2 5

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\blacktriangleright Numbers like 11010101 are very hard to read

⇒ Octal numbers

\blacktriangleright Numbers like 11010101 are very hard to read

⇒ Octal numbers

\blacktriangleright Numbers like 11010101 are very hard to read

⇒ Octal numbers

Binary number 1 1 0 1 0 1 0 1 —– ——– ——– Octal number 3 2 5 Position 2 1 0 Value 3×8^2 2×8^1 5×8^0 $192 \t 16 \t 5 = 213$

▶ ... but grouping **three** binary digits is a bit odd

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▶ Grouping 4 binary digits \rightarrow base $2^4 = 16$

 \blacktriangleright "Hexadecimal" (hex = Greek for six, decimus = Latin for tenth)

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- ▶ Grouping 4 binary digits \rightarrow base $2^4 = 16$
- \blacktriangleright "Hexadecimal" (hex = Greek for six, decimus = Latin for tenth)

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 \blacktriangleright Need characters for 10-15:

▶ Grouping 4 binary digits \rightarrow base $2^4 = 16$ \blacktriangleright "Hexadecimal" (hex = Greek for six, decimus = Latin for tenth) \triangleright Need characters for 10-15: use letters a-f Binary number 1 1 0 1 0 1 0 1 ———————————————————— Hexadecimal number d 5

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Examples

Examples

- \triangleright On all modern computers data is accessed in chunks of 8 bits: 1 byte
- \blacktriangleright Larger chunks of data ("words") are formed from multiple bytes:
	- \blacktriangleright 2 bytes = 16 bits
	- \blacktriangleright 4 bytes = 32 bits
	- \triangleright 8 bytes = 64 bits
- ▶ Modern CPUs have instructions for doing operations on word-sized data values

 \blacktriangleright The "primitive" C data types typically map onto machine word sizes

- ▶ ... but unfortunately, not in a way that's completely consistent across different machines and compilers
- \blacktriangleright "Typical" representations of C data types:

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(Note inconsistency in last row)

- \triangleright The stdint.h header file provides portable integer types providing an exact number of bits: int32_t, uint32_t, int64_t, uint64_t, etc.
- \triangleright Note that constant values are still a problem!
	- For example, 0×100000000 UL (2^{32}) is likely to be a valid on a 64-bit system but not on a 32-bit system

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 \blacktriangleright The "UL" suffix means "unsigned long"

Addresses

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- \triangleright Conceptually, memory (RAM) is a sequence of byte-sized storage locations
- Each byte storage location has an integer address
	- \triangleright 0 is the lowest address
	- \blacktriangleright Highest address determined by number of *address bits* processor uses:

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- ▶ 32-bit processors \Rightarrow addresses have 32 bits
- ▶ 64-bit processors \Rightarrow addresses have 64 bits
- \blacktriangleright 1 GB = 2^{30} , 1 TB = 2^{40}
- A 32-bit system can directly address 2^{32} bytes (4 GB)
	- \triangleright Not that much memory by today's standards!
- A 64-bit system can (in theory) directly access $2^{64} = 17.179.869.184$ GB $= 16,777,216$ TB
	- \blacktriangleright This is a *huge* address space
	- \triangleright Note that actual systems don't support that much physical memory
	- \blacktriangleright However, tens or hundreds of GB of physical memory is not uncommon

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- ► To store the value of an *n*-bit word in memory, *n*/8 contiguous bytes are used
- \blacktriangleright The address of the first byte is the address of the overall word
- \triangleright Typically, an *n*-byte word must have an address that is an exact multiple of n ("natural" alignment)
	- \triangleright For example, the first byte allocated for an 8-byte word must have an address that is an exact multiple of 8
- \triangleright Attempt to load or store an *n*-byte word at an address that is not a multiple of *n* is an *unaligned access*
	- \blacktriangleright Best case: access works, reduced performance
	- \triangleright Worst case: runtime exception that kills the program

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- \triangleright Pointers in C are just memory addresses!
- \triangleright The address-of operator (x) , when applied to a variable, yields a pointer to the variable (i.e., the address of the first memory byte that is part of the variable's storage)
- \blacktriangleright The dereference operator $(*)$, when applied to a pointer value (address), refers to the variable whose storage location is indicated by the address

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Example C program

```
#include <stdio.h>
#include <stdlib.h>
long g;
int main(void) {
  long* p = malloc(sizeof(long));
  long x;
  int a, b;
  short c, d, e, f;
  scanf("%ld %ld %ld %d %d %hd %hd %hd %hd",
        p, &g, &x, &a, &b, &c, &d, &e, &f);
  long sum = *p + g + x + a + b + c + d + e + f;
  print('"\text{Id}\n', sum);
 printf("%p\n%p\n%p\n%p\n%p\n%p\n%p\n%p\n%p\n",
         p, &g, &x, &a, &b, &c, &d, &e, &f);
  return 0;
}
```

```
$ gcc address.c
$ ./a.out
1 2 3 4 5 6 7 8 9
45
0x56142dfba260
0x56142c265018
0x7ffc7e6b2fd0
0x7ffc7e6b2fc8
0x7ffc7e6b2fcc
0x7ffc7e6b2fc0
0x7ffc7e6b2fc2
0x7ffc7e6b2fc4
0x7ffc7e6b2fc6
```

```
$ gcc address.c
$ ./a.out
1 2 3 4 5 6 7 8 9
45
0x56142dfba260 <-- address of malloc'ed buffer
0x56142c265018
0x7ffc7e6b2fd0
0x7ffc7e6b2fc8
0x7ffc7e6b2fcc
0x7ffc7e6b2fc0
0x7ffc7e6b2fc2
0x7ffc7e6b2fc4
0x7ffc7e6b2fc6
```

```
$ gcc address.c
$ ./a.out
1 2 3 4 5 6 7 8 9
45
0x56142dfba260
0x56142c265018 <-- address of global variable
0x7ffc7e6b2fd0
0x7ffc7e6b2fc8
0x7ffc7e6b2fcc
0x7ffc7e6b2fc0
0x7ffc7e6b2fc2
0x7ffc7e6b2fc4
0x7ffc7e6b2fc6
```

```
$ gcc address.c
$ ./a.out
1 2 3 4 5 6 7 8 9
45
0x56142dfba260
0x56142c265018
0x7ffc7e6b2fd0 <-- address of long variable on stack
0x7ffc7e6b2fc8
0x7ffc7e6b2fcc
0x7ffc7e6b2fc0
0x7ffc7e6b2fc2
0x7ffc7e6b2fc4
0x7ffc7e6b2fc6
```

```
$ gcc address.c
$ ./a.out
1 2 3 4 5 6 7 8 9
45
0x56142dfba260
0x56142c265018
0x7ffc7e6b2fd0
0x7ffc7e6b2fc2
0x7ffc7e6b2fc4
0x7ffc7e6b2fc6
```

```
0x7ffc7e6b2fc8 <-- address of int variable on stack
0x7ffc7e6b2fcc <-- address of int variable on stack
```
0x7ffc7e6b2fc0 (note addresses differ by 4)

```
$ gcc address.c
$ ./a.out
1 2 3 4 5 6 7 8 9
45
0x56142dfba260
0x56142c265018
0x7ffc7e6b2fd0
0x7ffc7e6b2fc8
0x7ffc7e6b2fcc
0x7ffc7e6b2fc00x7ffc7e6b2fc2 | <-- addresses of short variables on stack
0x7ffc7e6b2fc4 | (note addresses differ by 2)
0x7ffc7e6b2fc6
```
Bitwise operations

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 \triangleright Bitwise operations operate on the binary (bit-level) representation of an integer data value

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- \triangleright Logical operations: and, or, exclusive or, complement
- \triangleright Shifts: left shift, right shift

We can think of bit values (1 or 0) as being Boolean values (true or false)

Logical operations on bits **a** and **b**:

Logical negation ("complement") on a single bit **a**:

$$
\begin{array}{c|c}\n\text{a} & \text{a} \\
\hline\n0 & 1 \\
1 & 0\n\end{array}
$$

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- \blacktriangleright The C *bitwise operators* perform logical operations (and, or, xor, negation) on the *bits* of the binary representation(s) of integer values
	- \triangleright For example, $x \mid y$ computes a result whose bits are formed by applying the bitwise or operator (1) to each pair of bits in x and y
- \blacktriangleright Example code (bitwise or):

```
int x = 11;
int y = 40;
int z = x \mid y;printf("%d\n", z);
```
 \blacktriangleright What does this code do?

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$$
\begin{array}{rcl}\n & \text{int x = 11;} \\
 & \text{int y = 40;} \\
 & \text{int z = x | y;} \\
 & \text{printf("%d\n", z);} \\
 & \text{decimal} & \text{binary} \\
 & \text{x} & 11 = 8 + 2 + 1 & 00001011 \\
 & \text{y} & 40 = 32 + 8 & 00101000\n\end{array}
$$

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$$
\begin{array}{rcl}\n&\text{int x = 11;}\\
&\text{int y = 40;}\\
&\text{int z = x | y;}\\
&\text{printf("%d\n", z);}\\
&\text{decimal} &\text{binary}\\\n&\text{x} & 11 = 8 + 2 + 1 & 00001011\\
&\text{y} & 40 = 32 + 8 & 00101000\\
&\text{x | y 43 = 32 + 8 + 2 + 1 & 00101011}\n\end{array}
$$

Bit is 1 in result if corresponding bit is 1 in either operand value

 \triangleright Shifts move bits to the left or right in the binary representation of a data value

 \blacktriangleright Example code (left shift):

```
int x = 21;
int y = x \ll 3;
printf("%d\n", y);
```
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 \blacktriangleright What does this code do?

$$
int x = 21;\nint y = x << 3;\nprint("%d\n", y);\ndecimal binary
$$

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int $x = 21$; int $y = x \leq 3$; printf("%d\n", y); decimal binary $x = 21 = 16 + 4 + 1$ 00010101 $x \leq 3$ $168 = 128 + 32 + 8$ 10101000

Each bit in original value is shifted 3 places to the left; the lowest 3 bits of result become 0

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 \triangleright Bitwise operations (logical operations and shifts) are useful because they allow precise manipulations of data values at the level of individual bits:

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- \blacktriangleright Selecting arbitrary bits
- \blacktriangleright Clearing or setting arbitrary bits

Set bit n of variable x to 1 $x = (1 \leq x)$:

Set bit n of variable x to 0 $x \&= -(1 \le x);$

Get just the lowest n bits of variable x $x \& -(00 \le x)$

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